

	V1.0
	20210610

APT32S003 IFC



1

2.

3.

3.1 IFC

-

-

-

```
/******  
//ifc config  
//EntryParameter:NONE  
//ReturnValue:NONE  
/******  
void IFC_CONFIG(void)  
{  
    EnIFCClk;           // IFC  
    IFC->MR |= 0x10002; //      2  
}
```

3.2 IFC

```

        *(volatile unsigned int *) (FlashAdd+4*i)=DataBuffer;

        BufArray +=4;
    }

    //          3

    SetUserKey;

    IFC->CMR=0x06;           //

    IFC->FM_ADDR=FlashAdd;

    IFC->CR=0X01;           //Start Program

    while(IFC->CR!=0x0);    //

    //          4

    SetUserKey;

    IFC->CMR=0x01;           //

    IFC->FM_ADDR=FlashAdd;   //

    IFC->CR=0X01;           //Start Program

    while(IFC->CR!=0x0);    //

    //          5

    SetUserKey;

    IFC->CMR=0x02;           //

    IFC->FM_ADDR=FlashAdd;   //

    IFC->CR=0X01;           //Start Program

    while(IFC->CR!=0x0);    //

    //          6

    SetUserKey;

    IFC->CMR=0x01;           //

    IFC->FM_ADDR=FlashAdd;   //

    IFC->CR=0X01;           //Start Program

    while(IFC->CR!=0x0);    //

}

/*****/

//ifc config

//EntryParameter:NONE

//ReturnValue:NONE

/*****/

void IFC_CONFIG(void)

{

    EnIFCCLK;           // IFC

    IFC->MR |= 0x10002;   //          2

}

/*****/

/*****/

//APT32S003_init           /

//EntryParameter:NONE           /

//ReturnValue:NONE           /

/*****/

```

```

/*****/
/*****/

void APT32S003_init(void)
{
//-----/
//Peripheral clock enable and disable
//EntryParameter:NONE
//ReturnValue:NONE
//-----/

    SYSCON->PCER0=0xFFFFFFFF;           //PCLK Enable  0x00410071
    SYSCON->PCER1=0xFFFFFFFF;           //PCLK Enable

```



Page_ProgramData(0x10000040,8,&FlashDataArry0[0]);

3.3 IFC

```

/*****
//ReadFlashData fuction return Data arry save in Flash
//EntryParameter:RdStartAdd  DataLength  *DataArryPoint
//ReturnValue:NONE
*****/

void ReadDataArry_U8(unsigned int RdStartAdd,unsigned int DataLength,volatile unsigned char *DataArryPoint)
{
    unsigned int i;
    for (i=0;i<DataLength;i++)
    {
        if((i!=0)&&(i%4==0))
        {
            RdStartAdd +=4;
        }
        *DataArryPoint=*(U8_T*)(RdStartAdd+ (i%4));
        DataArryPoint++;
    }
}

/*****
//ifc config
//EntryParameter:NONE
//ReturnValue:NONE
*****/

void IFC_CONFIG(void)
{
    EnIFCClk;                //    IFC
    IFC->MR |= 0x10002;       //          2
}

unsigned char FlashDataArry0[8] = {1,2,3,4,5,6,7,8};
unsigned char DataBuffer0[8] = {0};
unsigned char BitIFCflag  ;

/*****

```

```

//main
/*****/

int main(void)
{
    APT32S003_init();
    while(1)
    {
        //SYSCON_IWD CNT_Reload();
        if(BitIFCflag)
        {
            BitIFCflag = 0;
            GPIO_Write_Low(GPIOA0,1);
            Page_ProgramData(0x10000040,8,&FlashDataArry0[0]);

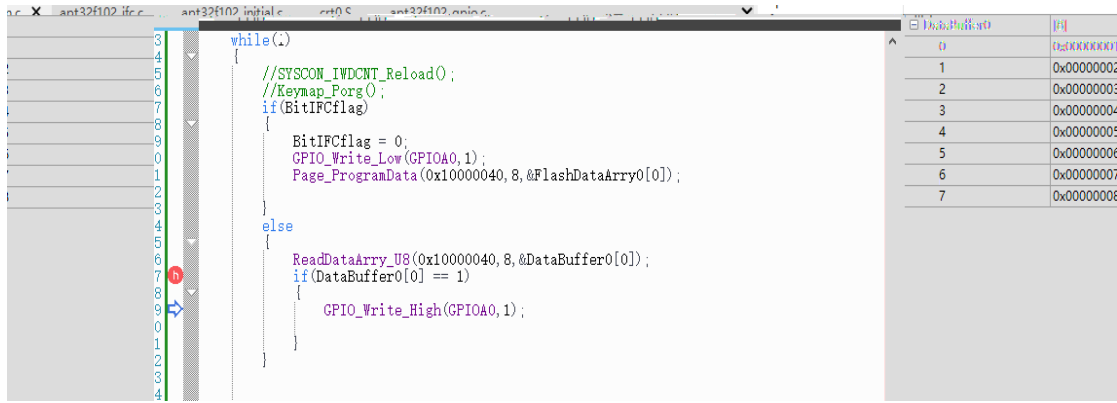
        }
        else
        {
            ReadDataArry_U8(0x10000040,8,&DataBuffer0[0]);
            if(DataBuffer0[0] == 1)
            {
                GPIO_Write_High(GPIOA0,1);
            }
        }
    }
}

```

- **ReadDataArry_U8();**



- **ReadDataArry_U8(0x10000040,8,&DataBuffer0[0]);**



4.